



DIPLOMA WING

# **RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA , BHOPAL**

SCHEME OF STUDIES & EXAMINATIONS (IMPLEMENTED FROM SESSION : JULY 2023)

SCHEME
OCBC JULY 2022/2023

NAME OF BRANCH
ET & TELECOMMUNICATION

BRANCH CODE
E03

SEMESTER
FOURTH (IV)

S.N.	PAPER CODE	SUBJECT CODE	SUBJECT NAME	THEORY COMPONENT								PRACTICAL COMPONENT						TOTAL CREDITS	TOTAL MARKS
				HRS PER WEEK	CREDITS	TERM WORK				THEORY PAPER		HRS PER WEEK	CREDITS	LAB WORK	PRACTICAL EXAM/VIVA				
						QUIZ/ASSIGNMENT	MID TERM TEST*		TOTAL	MARKS	DURATION				MARKS	DURATION			
							I	II											
1	7461	401	MICROCONTROLLER & APPLICATI.	4	4	10	10	10	30	70	03 Hrs.	2	1	20	30	03 Hrs.	5	150	
2	7462	402	ANALOG & DIGITAL COMM. SYSTEM	4	4	10	10	10	30	70	03 Hrs.	4	2	20	30	03 Hrs.	6	150	
3	7463	403	CONSUMER ELECTRONICS	4	4	10	10	10	30	70	03 Hrs.	0	0	0	0	0	4	100	
4	7464	404	ELECTRONIC EQUIPMENT MAINT.	3	3	10	10	10	30	70	03 Hrs.	0	0	0	0	0	3	100	
5	7465	405	LINEAR INTEGRATED CIRCUITS	3	3	10	10	10	30	70	03 Hrs.	4	2	20	30	03 Hrs.	5	150	
6			MINOR PROJECT	0	0	0	0	0	0	0	0	4	2	20	30	03 Hrs.	2	50	
7			ESSENCE OF INDIAN KNOWLEDGE & TRADITION	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
8			LIBRARY /SEMINAR/VISITS etc.	0	0	0	0	0	0	0	0	2	0	0	0	0	0	0	
TOTAL				20	18				150	350		16	7	80	120		25	700	

**NOTE -** (1)\* Two Best,out of Three Mid Term Tests (Progressive Tests) Marks should be entered here.

GRAND TOTAL OF CREDITS
25

GRAND TOTAL OF MARKS
700

**DIPLOMA WING**  
**RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL**  
**DIPLOMA IN ELECTRONICS & TELECOMMUNICATION ENGINEERING (E03)**  
**SEMESTER VI**

COURSE TITLE	:	MICROCONTROLLER AND APPLICATIONS
PAPER CODE	:	7461
SUBJECT CODE	:	401
THEORY CREDITS	:	04
PRACTICAL CREDITS	:	01

**Course Objective:**

The aim of this course is to help the student to understand:

- Basic Microprocessor and microcontroller
- Architecture of 8051
- Interfacing of 8051
- Introduction to ARM processor

**Course Content:**

Unit	Topics and Sub-topics	Hours	Marks
<b>Unit-1</b> Introduction to 8085 and 8051	<ul style="list-style-type: none"> <li>• Introduction &amp; Functional Block Diagram of microprocessor 8085</li> <li>• Introduction to Microcontrollers</li> <li>• Functional block diagram of 8051</li> <li>• Description of CPU, memory, Internal registers, Digital Input/Output ports, timers and counters, Clock circuit, Reset circuit, Stack and stack pointer, Input-output ports, Interrupts</li> <li>• Pin Diagram of 8051</li> <li>• Comparison of Microprocessors and Microcontrollers</li> <li>• Comparison between High Level Language &amp; Low Level Language</li> </ul>	<b>12</b>	<b>20</b>
<b>Unit 2-</b> Programming with 8051	<ul style="list-style-type: none"> <li>• Types of Addressing modes</li> <li>• Classification of 8051 instruction set: <ul style="list-style-type: none"> <li>• Data transfer instruction</li> <li>• Arithmetic Instructions</li> <li>• Logical instructions</li> <li>• Bit manipulations instructions</li> <li>• Branching instructions (unconditional and conditional)</li> <li>• Machine instruction</li> <li>• I/O Programming instructions</li> </ul> </li> <li>• Introduction to Interrupts in 8051: Internal and External</li> <li>• Simple Assembly Language program based on Arithmetic, Logic &amp; Data transfer instructions</li> </ul>	<b>12</b>	<b>20</b>

<b>Unit 3-</b> Components of MCS51	Concept and functions of following : <ul style="list-style-type: none"> <li>• Timers and Counters</li> <li>• Interrupt control</li> <li>• Serial I/O Ports</li> </ul>	<b>12</b>	<b>10</b>
<b>Unit 4</b> Interfacing of MCS51	<ul style="list-style-type: none"> <li>• Interfacing of single LED with 8051 using Assembly Language</li> <li>• Concept and diagram of following interfacing with MCS51:</li> <li>• 4X4 Hex keyboard</li> <li>• 4 Digit Seven segment Display</li> <li>• 16X2 LCD</li> <li>• Sensor-Temperature (LM35)</li> <li>• ADC 0808 chip</li> <li>• PPI 8255 chip</li> <li>•</li> </ul>	<b>12</b>	<b>10</b>
<b>Unit 5-</b> ARM processor	<ul style="list-style-type: none"> <li>• Features and comparison of CISC and RISC processors</li> <li>• Features of ARM microcontrollers</li> <li>• Architecture and features of ARM7</li> </ul>	<b>12</b>	<b>10</b>

#### REFERENCES / SUGGESTED LEARNING RESOURCES:

S. No.	Title of Book	Author	Publication
1.	Microcontrollers and Applications	Santanu Chattopadhyay	AICTE
2.	The 8051 Micro Controller and Embedded Systems	Muhammad Ali Mazidi & Janice Gilli Mazidi, R.D. Kinely	PHI Pearson Education, 5th Indian reprint
3.	Microprocessor and Microcontrollers	Krishna Kant	Eastern Company Edition, Prentice Hall of India, New Delhi
4.	Microprocessor & Microcontroller Architecture: Programming & Interfacing using 8085, 8086, 8051	Soumitra Kumar Mandal	McGraw Hill Edu,
5.	ARM Developer's Guide. UM10139 LPC214X User manual	Andrew N. Sloss, Dominic Symes, Chris Wright	User manual

#### SUGGESTED PRACTICALS/ EXERCISES :

S.No.	Practical Outcomes (PrOs)
1.	Identifying pins of 8085, 8051
2.	Programming 8051 with Arithmetic logic instructions [Assembly]
3.	Programming 8051 with Data Transfer and Branching instructions. [Assembly]

4.	Programming 8051 using Ports [Assembly or C]
5.	Interfacing 8051 single LED with Pushbutton switch [Assembly or C]
6.	Interfacing 8051 seven segment Display [Assembly or C]
7.	Delay generation using Timer [Assembly or C]
8.	Interfacing LCD Display with 8051 [Assembly or C]
9	Interfacing with 4x4 Hex Keypad with 8051[Assembly or C]
10	Programming 8051 using ADC [Assembly or C]
11	Identifying pins of ARM Based LPC2148 microcontroller

#### **SUGGESTED SOFTWARE/LEARNING WEBSITES:**

**[https://www.tutorialspoint.com/microprocessor/microcontrollers\\_application.htm](https://www.tutorialspoint.com/microprocessor/microcontrollers_application.htm)**

**[www.arm.com](http://www.arm.com)**



**DIPLOMA WING**  
**RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL**  
**DIPLOMA IN ELECTRONICS & TELECOMMUNICATION ENGINEERING (E03)**

**SEMESTER IV**

COURSE TITLE	:	<b>ANALOG AND DIGITAL COMMUNICATION SYSTEMS</b>
PAPER CODE	:	7462
SUBJECT CODE	:	402
THEORY CREDITS	:	04
PRACTICAL CREDITS	:	02

**Course Objective:**

By the end of the course, students should be able to:

1. Understand the structure and function of a basic digital communication system
2. Apply sampling and quantization techniques in pulse modulation systems
3. Demonstrate and analyze baseband and pulse modulation methods
4. Compare ASK, FSK, and PSK modulation techniques with practical waveform analysis
5. Evaluate the performance of digital communication systems using BER and Eye Diagrams

**Course Content:**

Unit	Topics and Sub-topics	Hours	Marks
<b>Unit 1: Fundamentals of Digital Communication</b>	Analog vs. Digital Communication, Components of a digital communication system (block diagram overview) Advantages and limitations of digital systems Review of signals and systems: periodic/non-periodic, deterministic/random Introduction to time domain vs. frequency domain (qualitative) Noise and Signal-to-Noise Ratio (SNR) basics Concept of bandwidth and data rate (qualitative)	<b>12</b>	<b>15</b>
<b>Unit 2: Sampling and Quantization</b>	Sampling Theorem for low-pass signals Types of sampling: Ideal, Natural, Flat-top Aliasing and Anti-aliasing filters (basic concept) Quantization: Uniform and Non-uniform Quantization noise and SNR Concept of Companding ( $\mu$ -law and A-law only)	<b>10</b>	<b>10</b>
<b>Unit 3: Pulse Modulation and Baseband Transmission</b>	Pulse Code Modulation (PCM): Generation and reconstruction Delta Modulation (DM) and Adaptive Delta Modulation (ADM): Concept and comparison Time Division Multiplexing (TDM): Concept and simple frame structure	<b>14</b>	<b>15</b>

	Inter-symbol interference (ISI): Concept only Eye diagram: interpretation and use (qualitative)		
<b>Unit 4: Digital Carrier Modulation Techniques</b>	Need for carrier modulation in digital systems Amplitude Shift Keying (ASK): waveform and detection Frequency Shift Keying (FSK): waveform and detection Phase Shift Keying (PSK): waveform and detection Basic idea of QPSK (no math, just concept and waveform) Bandwidth comparison and noise performance (qualitative) Block diagram of a digital receiver Concept of matched filtering (qualitative only) Bit Error Rate (BER): Definition and significance Factors affecting BER (noise, bandwidth, signal power) Basics of synchronization: timing and clock recovery	<b>12</b>	<b>15</b>
<b>Unit 5: Information Theory and Coding</b>	Concept of Information and Bit as a unit of information, bit representation and basic logic, Concept of Bandwidth and Data Rate, Meaning of entropy as measure of uncertainty redundancy in messages, removal of redundancy Line Coding: NRZ, RZ, Manchester – characteristics and need Source Coding: Huffman, Shannon-Fano Importance and basic understanding of Shannon Capacity Formula Relation of bandwidth, SNR, and data rate Introduction to secure communication: Basic concept of encryption	<b>12</b>	<b>15</b>

#### SUGGESTED LEARNING RESOURCES:

S. No.	Title of Book	Author(s)	Publication
1	Principles of Communication Systems	Taub and Schilling	McGraw-Hill Education
2	Analog and Digital Communication	R. P. Singh and S. D. Sapre	Tata McGraw-Hill
3	Communication Systems	Sanjay Sharma	S. K. Kataria & Sons
4	Modern Digital and Analog Communication Systems	B. P. Lathi and Zhi Ding	Oxford University Press
5	Digital Communication: Fundamentals and Applications	Bernard Sklar and P. K. Ray	Pearson Education
6	Electronic Communication Systems	George Kennedy and Bernard Davis	McGraw-Hill Education
7	Communication Systems	Simon Haykin	John Wiley & Sons

### **SUGGESTED LIST OF PRACTICALS:**

- 1.To implement Pulse Code Modulation (PCM) and observe sampled, quantized, and encoded signals using trainer kit.
- 2.To perform Differential Pulse Code Modulation (DPCM) using trainer kit and analyze their behaviour.
- 3.To perform Delta Modulation (DM) using trainer kit and analyze their behaviour.
- 4.To perform Adaptive Delta Modulation (ADM) using trainer kit and analyze their behaviour.
- 5.To implement Time Division Multiplexing (TDM) and demultiplexing using trainer kit and observe frame synchronization.
- 6.To generate and observe ASK waveforms using modulation kit and verify signal characteristics.
- 7.To generate and observe FSK waveforms using modulation kit and verify signal characteristics.
- 8.To generate and observe PSK waveforms using modulation kit and verify signal characteristics.
- 9.To generate and compare line-coded waveforms (NRZ, RZ, and Manchester) and observe their characteristics using CRO or simulation tools.
10. To simulate and observe Eye Diagrams for baseband signals and analyze the effects of timing jitter and inter-symbol interference (ISI).



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**DIPLOMA IN ELECTRONICS & TELECOMMUNICATION ENGINEERING (E03)**

**SEMESTER IV**

COURSE TITLE	:	CONSUMER ELECTRONICS
PAPER CODE	:	7463
SUBJECT CODE	:	403
THEORY CREDITS	:	04
PRACTICAL CREDITS	:	00

**Course Objective:** The aim of this course is to help the student to understand different microphones and speakers, troubleshoot the audio system, understand the operating principle of color TV, troubleshoot electronics appliances and understand electromechanical system used Electronic Appliances.

**Course Content:**

Unit	Topics and Sub-topics	Hours	Marks
<b>Unit-1 Audio Fundamentals and Devices</b>	<b>Basic characteristics of sound signal:</b> <b>Audio level metering</b> <b>Decibel level in acoustic measurement</b> <b>Fidelity</b> <b>Sensitivity</b> <b>selectivity</b> <b>Working principle and characteristics of Microphone,</b> <b>Types of Microphones: carbon, moving coil and crystal Microphone.</b> <b>Working principle and characteristics of speaker.</b> <b>Type of speaker: PMMC, Direct radiating and indirect radiating speaker</b> <b>Principle of Digital and optical sound recording</b>	<b>12</b>	<b>15</b>
<b>Unit 2 –Audio Systems</b>	<b>Introduction of home theatre sound system and surround sound</b> <b>block diagram of Digital console using ICs TDA 7021T</b> <b>Block diagram of FM tuner</b> <b>Introduction and block diagram of PA address system.</b> <b>Planning of PA system for public meeting and auditorium.</b>	<b>10</b>	<b>10</b>
<b>Unit 3- Television Systems</b>	<b>Monochrome TV standards:</b> <b>scanning process: horizontal and vertical scanning</b> <b>aspect ratio</b> <b>persistence of vision and flicker</b> <b>interlace scanning</b> <b>picture resolution</b> <b>Composite video signal</b> <b>Colour TV standards: colour theory, hue, brightness, saturation</b> <b>Luminance, chrominance</b> <b>construction and working of TV camera: vidicon and plumbicon</b> <b>Transmission standards for TV system in India</b>	<b>12</b>	<b>15</b>

<b>Unit 4- Television Receivers and Video Systems</b>	<b>Block diagram of PAL-D colour TV receiver Introduction and Comparison of Digital TVs: - LCD, LED, PLASMA, HDTV, 3-D TV, projection TV Block Diagram of DTH receiver Introduction of Video interface, Digital Video Interface, SDI, HDMI Block diagram of CD and DVD player.</b>	<b>14</b>	<b>15</b>
<b>Unit 5- Home / Office Appliances</b>	<b>Block Diagram and working of FAX Photocopier Microwave Oven Washing Machine Air conditioner Refrigerator Introduction to Digital camera and Cam coder</b>	<b>12</b>	<b>15</b>

#### SUGGESTED LEARNING RESOURCES:

##### References:

1. Consumer Electronics : Amit m Joshi, Maulin M Joshi and Urvashi Prakash Shukla , AICTE
2. Consumer Electronics : Bali S.P. Pearson Education India,2010 , latest edition
3. Audio video systems : principle practices & troubleshooting Bali R and Bali , S.P Khanna Book Publishing Co. (P) Ltd., 2010Delhi , India, latest edition
4. Modern Television practices: Gulati R.R. New Age International Publication (P) Ltd. New Delhi Year 2011, latest edition
5. Audio video systems: Gupta R.G.Tata Mc graw Hill, New Delhi, India 2010, latest edition
6. Mastering Digital Television:. Whitaker Jerry & Benson Blair Tata Mc graw Hill, New Delhi, India 2010
7. Standard handbook of Audio engineering : Whitaker Jerry & Benson Blair McGraw-Hill Professional, 2010 , latest edition.

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COURSE TITLE	:	<b>Electronics Equipment Maintenance</b>
PAPER CODE	:	7464
SUBJECT CODE	:	404
THEORY CREDITS	:	03
PRACTICAL CREDITS	:	00

**Course Objective:** The aim of this course is to understand the procedure of troubleshooting electronic equipment, test passive components, identify causes of failures in semiconductor devices and fault diagnosis, repair surface mount assemblies and PCBs.

**Course Content:**

Unit	Topics and Sub-topics	Hours	Marks
<b>Unit-1: Fundamental of troubleshooting procedures</b>	<ul style="list-style-type: none"><li>• Basics of Electronic circuits,</li><li>• Types of PCB: Single, Double, Multilayer,</li><li>• Reading drawing &amp; diagram: Block diagram, Circuit diagram, Wiring diagram,</li><li>• Equipment failures, Causes of equipment failures,</li><li>• Steps of Troubleshooting Process: Fault establishment, Fault location, Fault correction,</li><li>• Introduction of Electronic test equipment: Analog multimeter, Digital multimeter, Oscilloscope,</li><li>• Troubleshooting techniques: Functional area approach, Split half method, Divergent paths, Convergent paths, Feedback paths, Switching paths,</li><li>• Steps for Approaching components for tests,</li><li>• Temperature sensitive Intermittent problems,</li><li>• Corrective action: Arranging replacement parts, Component replacement, Performance check, Replacement of circuit boards,</li><li>• Situations where repairs should not be attempted.</li></ul>	<b>10</b>	<b>14</b>
<b>Unit 2 – Passive Components and Their Testing</b>	<ul style="list-style-type: none"><li>• Introduction to Passive Components: Resistors, Capacitors, Inductors,</li><li>• Failures in fixed resistors, testing of resistors,</li><li>• variable resistors (Potentiometer) , failures in potentiometers, testing of potentiometers,</li><li>• Basics of LDRs and Thermistors,</li><li>• Types of capacitors: Paper, Mica, Ceramic, Plastic, Electrolytic,</li><li>• Performance of capacitors, Failures in capacitors, testing of capacitors, Precautions While testing capacitors,</li><li>• Types of variable capacitor: Button, Tubular</li><li>• Testing and measurement of inductors,</li></ul>	<b>10</b>	<b>14</b>

<b>Unit 3- Testing of Semiconductor Devices</b>	<ul style="list-style-type: none"> <li>• Testing and Causes of failure in Semiconductor Devices: - <ul style="list-style-type: none"> <li>▪ Zener diode,</li> <li>▪ Varactor diode,</li> <li>▪ Varistor,</li> <li>▪ Light Emitting Diodes (LED),</li> <li>▪ Photo-diode,</li> <li>▪ Bi-polar Junction Transistors (BJT),</li> <li>▪ Field-effect Transistors (FET),</li> <li>▪ Silicon Controlled Rectifier (SCR),</li> <li>▪ TRIAC</li> </ul> </li> <li>• Fault diagnosis in Op-amp circuits</li> </ul>	<b>08</b>	<b>14</b>
<b>Unit 4- Troubleshooting Digital Circuits</b>	<ul style="list-style-type: none"> <li>• List of Logic IC Series,</li> <li>• Introduction of Packages in Digital ICs: DIP, Flat pack,</li> <li>• Identification of IC,</li> <li>• Handling ICs: Removing and Mounting IC in Circuit board,</li> <li>• Digital troubleshooting methods – typical faults, testing digital ICs with pulse generators,</li> <li>• Digital IC troubleshooters: Logic clip, Logic Probe, Logic Pulser,</li> <li>• Special consideration for fault diagnosis in digital circuits, Handling precautions for ICs sensitive to static electricity,</li> <li>• Microprocessor troubleshooting techniques, testing microprocessors,</li> <li>• Testing of RAM and ROM,</li> </ul>	<b>10</b>	<b>14</b>
<b>Unit-5- Repair of Surface Mount Assemblies</b>	<ul style="list-style-type: none"> <li>• Introduction of Surface Mount Technology and surface mount devices,</li> <li>• Surface Mount Semiconductor packages – SOIC, SOT, LCCC, PLCC, Cylindrical Diode Packages,</li> <li>• Packaging of Passive Components as SMDs,</li> <li>• Repairing Surface Mount PCBs: Heating Methods, Removal and replacement of SMD.</li> </ul>	<b>07</b>	<b>14</b>

#### SUGGESTED LEARNING RESOURCES:

##### References:

<b>S. No.</b>	<b>Title of Book</b>	<b>Author</b>	<b>Publication</b>
1	Modern Electronic Equipment: Troubleshooting, Repair and Maintenance	R. S. Khandpur	TMH 2006
2	Electronic Equipment Maintenance	Dr. Chanchal Sharma	AICTE
3	Electronic Instruments and Systems: Principles, Maintenance and Troubleshooting	R. G. Gupta	Tata McGraw Hill Edition 2001
4	Student Reference Manual for Electronic Instrumentation Laboratories	David L Terrell	Butterworth-Heinemann
5	Electronic Testing and Fault Diagnosis	G. C. Loveday, A. H	Wheeler Publishing



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**DIPLOMA IN ELECTRONICS& TELECOMMUNICATION ENGINEERING (E03)**  
**SEMESTER IV**

<b>COURSE TITLE</b>	<b>:</b>	<b>LINEAR INTEGRATED CIRCUITS</b>
<b>PAPER CODE</b>	<b>:</b>	<b>7465</b>
<b>SUBJECT CODE</b>	<b>:</b>	<b>405</b>
<b>TIME DURATION FOR THEORY CLASS</b>	<b>:</b>	<b>45 Hrs.</b>

**Course Objective:** To help diploma students understand IC fabrication processes, operational amplifier applications, analog multiplier and PLL operations and special function ICs with their practical applications in electronic systems

**COURSE CONTENT:**

<b>Unit</b>	<b>Topics and Sub-Topics</b>	<b>Duration</b>	<b>Marks</b>
<b>UNIT 1 - FUNDAMENTALS OF IC TECHNOLOGY AND OP-AMP BASICS</b>	<b>1.1 Introduction to ICs</b> <ul style="list-style-type: none"> <li>Advantages of ICs over discrete components</li> </ul> <b>1.2 Manufacturing process of monolithic ICs</b> <ul style="list-style-type: none"> <li>Wafer preparation</li> <li>Epitaxial growth</li> <li>Oxidation, photolithography</li> <li>Diffusion, Metallization and Packaging</li> </ul> <b>1.3 Construction of monolithic components</b> <ul style="list-style-type: none"> <li>Bipolar transistor: structure, fabrication steps, and cross-sectional view</li> <li>Integrated Resistors: diffused and thin film types</li> </ul> <b>1.4 Current mirror and current sources</b> <ul style="list-style-type: none"> <li>Basic current mirror circuit and operation</li> </ul> <b>1.5 Voltage sources and references</b> <ul style="list-style-type: none"> <li>Zener diode voltage reference: concept</li> </ul> <b>1.6 Introduction to Operational amplifier IC 741</b> <ul style="list-style-type: none"> <li>Block diagram with stage identification</li> <li>DC characteristics: input offset voltage, bias current, and offset current</li> <li>AC characteristics: gain, bandwidth, slew rate, settling time</li> <li>Open loop and closed loop configurations: differences</li> </ul>	<b>09 hrs.</b>	<b>14</b>

<b>Unit</b>	<b>Topics and Sub-Topics</b>	<b>Duration</b>	<b>Marks</b>
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<b>UNIT 2 – BASIC OP-AMP CIRCUITS AND APPLICATIONS</b>	<b>2.1 Basic Op-Amp Circuits</b> <ul style="list-style-type: none"> <li>Inverting amplifier: circuit diagram, working principle, and gain equation</li> <li>Non-inverting amplifier: circuit diagram, working principle, and gain equation</li> <li>Sign Changer: circuit and operation</li> <li>Scale Changer: circuit and operation</li> <li>Voltage Follower: circuit, unity gain buffer operation and applications</li> </ul> <b>2.2 Signal Converters</b> <ul style="list-style-type: none"> <li>V-to-I converter: circuit diagram with floating load</li> <li>I-to-V converter: circuit diagram and transfer characteristics</li> </ul> <b>2.3 Adder and Subtractor Circuits for Two Inputs</b> <ul style="list-style-type: none"> <li>Summing amplifier (adder): circuit diagram, working principle, and output equation</li> <li>Subtractor: circuit diagram, working principle, and output equation</li> <li>Instrumentation amplifier: circuit diagram &amp; working</li> </ul> <b>2.4 Integrator &amp; Differentiator</b> <ul style="list-style-type: none"> <li>Circuit diagram and operation</li> </ul>	<b>09 hrs.</b>	<b>14</b>
<b>UNIT 3 – ADVANCED OP-AMP APPLICATIONS AND CIRCUIT BUILDING BLOCKS</b>	<b>3.1 Differential amplifier using Op-Amp</b> <ul style="list-style-type: none"> <li>Basic differential pair: circuit and operation</li> <li>CMRR and PSRR: definitions and importance</li> </ul> <b>3.2 Comparators and Waveform Circuits</b> <ul style="list-style-type: none"> <li>Comparators: basic circuit and zero crossing detection</li> <li>Schmitt trigger: circuit diagram, and waveform conversion</li> <li>Precision rectifier: circuit diagrams for half-wave and full-wave precision rectification</li> <li>Peak detector: circuit diagram and hold operation</li> </ul> <b>3.3 Active Filters</b> <ul style="list-style-type: none"> <li>Low-pass Butterworth filter: circuit diagram and frequency response</li> <li>High-pass Butterworth filter: circuit diagram and frequency response</li> <li>Band-pass Butterworth filter: circuit diagram and frequency response</li> </ul>	<b>09 hrs.</b>	<b>14</b>
<b>UNIT 4 – ANALOG MULTIPLIERS AND SIGNAL PROCESSING CIRCUITS</b>  <b>UNIT 4 –</b>	<b>4.1 Analog Multiplier Circuits</b> <ul style="list-style-type: none"> <li>Analog Multiplier using Emitter Coupled Transistor Pair: circuit diagram and operation</li> <li>Gilbert Multiplier cell: circuit diagram, quadrant operation, and transfer function</li> </ul> <b>4.2 Commercial Analog Multiplier ICs</b> <ul style="list-style-type: none"> <li>IC AD534: pin diagram, specifications, applications: modulation, demodulation</li> </ul> <b>4.3 Sample and Hold Circuits</b>	<b>09 hrs.</b>	<b>14</b>

<b>ANALOG MULTIPLIERS AND SIGNAL PROCESSING CIRCUITS</b>	<ul style="list-style-type: none"> <li>• Basic sample and hold: circuit diagram and operation</li> </ul> <b>4.4 Phase Locked Loop Fundamentals</b> <ul style="list-style-type: none"> <li>• Basic PLL block diagram and operation</li> <li>• Closed loop analysis: lock range, capture range, and stability considerations</li> <li>• Monolithic PLL IC 565: pin diagram, functional blocks</li> <li>• Application of PLL AM &amp; FM demodulation</li> </ul>		
<b>UNIT 5 – WAVEFORM GENERATORS AND SPECIAL FUNCTION ICs</b>	<b>5.1 Waveform Generator Circuits</b> <ul style="list-style-type: none"> <li>• Timer IC 555: pin diagram, internal block diagram and application: Multivibrators- astable, monostable, and bistable circuit diagrams and waveform</li> </ul> <b>5.2 Oscillator Circuits using Op-Amp</b> <ul style="list-style-type: none"> <li>• Sine-wave generators: Wien bridge and phase shift oscillator circuits with circuit diagram and operation</li> <li>• Triangular wave generator: circuit diagram and operation</li> <li>• Saw-tooth wave generator: circuit diagram and operation</li> </ul> <b>5.3 Function Generator ICs</b> <ul style="list-style-type: none"> <li>• ICL8038 function generator: pin diagram, block diagram</li> <li>• IC Voltage regulators – Three terminal fixed and adjustable voltage regulators - IC 7805, 7905, 723</li> </ul>	<b>09 hrs.</b>	<b>14</b>

### **LINEAR INTEGRATED CIRCUITS LAB**

List of Practicals/Experiments:

1. Operational Amplifiers (IC741)-Characteristics
2. Inverting and Non inverting Amplifiers
3. Summer, Difference Amplifier and Instrumentation Amplifier
4. Waveform shaping circuits using opamp
5. Comparator and Schmitt trigger
6. Waveform Generation using Op-Amp (IC741).
7. Applications of Timer IC555.
8. Frequency Response of Active filters.
9. Study and application of PLL IC's
10. Op-Amp voltage Regulator- IC 723

**Textbooks/References:**

- Linear Integrated Circuit – Dr. Battula Tirumala Krishna, AICTE Publication
- Design with operational amplifiers and analog integrated circuits, 3<sup>rd</sup> Edition – Sergio Franco, Tata McGraw-Hill, 2007
- Linear Integrated Circuits – D Roy Choudhry, Shail Jain, New Age International Pvt. Ltd
- System design using Integrated Circuits – B S Sonde, New Age Pub, 2<sup>nd</sup> Edition, 2001
- Analysis and Design of Analog Integrated Circuits – Gray and Meyer, Wiley International, 2005
- OP-AMP and Linear ICs – Ramakant A Gayakwad, Prentice Hall / Pearson Education, 4<sup>th</sup> Edition, 2001
- Linear Integrated Circuits – R. Anand, Khanna Book Publishing
- Operational Amplifier and Linear Integrated Circuits – K Lal Kishore, Pearson Education, 2006



**DIPLOMA WING**  
**RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL**

DIPLOMA IN ET. & TELECOMMUNICATION ENGINEERING

SEMESTER - IV

COURSE TITLE	:	MINOR PROJECT
PAPER CODE	:	--
SUBJECT CODE	:	--
TREORY CREDITS	:	00
PRACTICAL CREDITS	:	02

MINOR PROJECT –

Evaluation is based on work done, quality of report  
performance in viva-voce, presentation etc.

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## DIPLOMA WING

RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL

DIPLOMA IN ET. & TELECOMMUNICATION ENGINEERING (E03)

SEMESTER - IV

COURSE TITLE	:	ESSENCE OF INDIAN KNOWLEDGE & TRADITION
PAPER CODE	:	--
COURSE CODE	:	--
THEORY CREDITS	:	00
PRACTICAL CREDITS	:	00

### CourseContent:

BasicStructureofIndianKnowledgeSystem:

(i) OSद, (ii) उन्न OSद (आय OSद, /kuqosnxU/kosnLFkkत्रR;  
आदद) (iii) osnkax(f'k{kkdYuuu:rO;kdj.kT;ksur"knkan),

(iv) उन्नइग(/keZj~hj~kalk, नqjk.k, rdfel=)

- ModernScienceandIndianKnowledgeSystem
- YogaandHolisticHealthcare
- CaseStudies.

### SUGGESTEDTEXT/REFERENCEBOOKS:

S.No.	TitleofBook	Author	Publication
1.	CulturalHeritageofIn-dia-CourseMaterial	V.Sivaramakrishna	BharatiyaVidyaBhavan, Mumbai, 5th Edition, 2014
2.	ModernPhysicsandVedant	SwamiJitatmanand	BharatiyaVidyaBhavan
3.	ThewaveofLife	FritzofCapra	
4.	TaoofPhysics	FritzofCapra	
5.	TarkasangrahaofAnnamBhatta, International	V N Jha	ChinmayFoundation, Velliarnad, Amak u, am

6.	ScienceofConsciousnessPsychotherapyandYogaPractices	RNJha	VidyanidhiPrakasham,Delhi,2016
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