



## RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL

SCHEME OF STUDIES & EXAMINATIONS (IMPLEMENTED FROM SESSION: JULY 2023)

SCHEME
OCBC JULY 2022/2023

NAME OF BRANCH
ET. & TELECOMMUNICATION

BRANCH CODE E03

SEMESTER THIRD (III)

						T	HEOI	RY CO	OMP	ONENT		PR	ACTI	CAL (	COMF	PONENT		
				EK		TE	RM	WOF	RK	THEO	RY PAPER	¥			PRACTICAL EXAM/VIVA		ITS	IKS
S.N.	PAPER CODE	SUBJECT CODE	SUBJECT NAME	HRS PER WEEK	CREDITS	QUIZ/ASSIGNMENT		ID RM ST*	TOTAL	MARKS	DURATION	HRS PER WEEK	CREDITS	LAB WORK	MARKS	DURATION	TOTAL CREDITS	TOTAL MARKS
1	7456	301	ET. DEVICES & CIRCUITS	3	3	10	10	10	30	70	03 Hrs.	4	2	20	30	03 Hrs.	5	150
2	7457	302	DIGITAL SYSTEM	3	3	10	10	10	30	70	03 Hrs.	4	2	20	30	03 Hrs.	5	150
3	7458	303	ET. MEASUREMENT & INST.	3	3	10	10	10	30	70	03 Hrs.	2	1	20	30	03 Hrs.	4	150
4	7459	304	ELECTRIC CIRCUITS & NETWORK	4	4	10	10	10	30	70	03 Hrs.	0	0	0	0	0	4	100
5	7460	305	PRINCIPLES OF ET. COMM.	3	3	10	10	10	30	70	03 Hrs.	4	2	20	30	03 Hrs.	5	150
6			**SUMMER INTERNSHIP- I	0	0	0	0	0	0	0	0	0	2	20	30	03 Hrs.	2	50
7			PROFESSIONAL DEVELOPMENT	0	0	0	0	0	0	0	0	2	0	0	0	0	0	0
8			***RECOVERY CLASSES/LIBERARY etc.	0	0	0	0	0	0	0	0	4	0	0	0	0	0	0
			TOTAL	16	16				150	350		20	9	100	150		25	750

**NOTE -** (1)\* Two Best, out of Three Mid Term Tests (Progressive Tests) Marks should be entered here.

- (2)\*\* 3-4 Weeks Summer Internship after II Semester.
- (3)\*\*\*To recover courses if session delays due to summer internship.

(	GRAND TOTAL OF CREDITS
	25

GRAND TOTAL OF MARKS 750



DIPLOMA IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (E03) / DIPLOMA IN ELECTRONICS ENGINEERING (E06)

#### SEMESTER III

COURSE TITLE	:	ELECTRONICS DEVICES AND CIRCUITS
PAPER CODE	:	7456
SUBJECT CODE	:	301
TREORY CREDITS	:	03
PRACTICAL CREDITS	:	02

**Course objective:** The aim of this course is to help the student to attain the following knowledge through various teaching- learning experiences:

- > Identify different Electronic components.
- ➤ Learn working principle and application of different Electronic components.
- > Learn working principle of rectifiers.
- ➤ Learn working principle of Amplifiers and Oscillators

Unit	Sub – Topic	Hours	Marks
Unit 1	<ul> <li>Definition of Extrinsic &amp; Intrinsic, N-type &amp; p-type semiconductor</li> </ul>	15	16
Semiconduc	<ul> <li>PN Junction Diode , operation of PN Junction diode (Concept of</li> </ul>		
tor and	Depletion layer, Drift current, Diffusion current )		
Diodes	<ul> <li>Biasing (Forward and Reverse Bias), V- I Characteristics of Diode, Shockley Diode Equation (Equation only)</li> </ul>		
	<ul> <li>Application of Diode as Rectifiers – Half Wave and Full Wave Rectifier,</li> </ul>		
	(Circuit Diagram, Working, Input, Output Waveform).		
	<ul> <li>Definition of Rectifier Efficiency, Ripple Factor &amp; PIV, Comparison of</li> </ul>		
	Rectifier on the basis of Rectifier Efficiency, Ripple Factor & PIV ,		
	Applications of Rectifiers.		
	Use of Filters – C, L, LC & PI Filters.		
	<ul> <li>Zener Diode – construction, Principle, working &amp; VI characteristics,</li> </ul>		
	<ul> <li>Application of Zener Diode as a Voltage Regulator</li> </ul>		
	<ul> <li>Block Diagram of regulated DC power supply.</li> </ul>		
Unit 2	• Bipolar Junction Transistor (BJT), Types of BJT, BJT Structure(Area,	15	16
Bipolar	Doping)		
Junction	<ul> <li>Four possible Biasing condition of a BJT (Active region, Saturation</li> </ul>		
Transistor	region, Cutoff region, Inverse Active Region), Concept of Q- Point.		
(BJT)	NPN and PNP Transistor – Operation and characteristics		
	Applications of BJT as a switch.		
	Common Base (CB) Configuration – characteristics and working		
	Common Emitter (CE) Configuration – characteristics and working		
	Common Collector (CC) Configuration – characteristics and working		
	Relation between Current Gain ( Alpha, Beta, Gamma) ,		
	Numerical based on current Gain     Section 1 Control Con		
	Comparison between CB, CE, CC on Basis of Current Gain, Voltage      Cain, Input Impedance, Output Impedance, Application		
Unit 3	<ul> <li>Gain, Input Impedance, Output Impedance, Application</li> <li>Introduction of Field Effect Transistors (FET)- Working Principle,</li> </ul>	10	16
Field Effect	Comparison of BJT & FET as Current controlled & Voltage Controlled	10	10
. ICIG EIICCC	comparison of Bit & 121 as current controlled & Voltage Controlled		

Transistors (FET)	<ul> <li>device.</li> <li>Classification of FET,</li> <li>JFET - Construction, Circuit Symbols, Operation, Drawback of JFET</li> <li>MOSFET - Types of MOSFET, Construction, Circuit Symbols Working, Characteristics( Drain Characteristics, Transfer Characteristics)</li> <li>MOSFET as a Switch</li> </ul>		
Unit 4 SCR,DIAC & TRIAC, UJT	<ul> <li>Concept of CMOS</li> <li>Introduction to SCR, DIAC &amp; TRIAC:</li> <li>SCR – Construction, Symbol, operation, , V-I characteristics (Concept of Holding Current &amp; Latching Current), List Applications</li> </ul>	10	10
TRIAC, OII	<ul> <li>TRIAC - Construction, Symbol, operation, V- I characteristics , List applications</li> <li>Comparison of SCR, TRIAC</li> <li>DIAC - Construction, operation, V-I characteristics</li> <li>UJT – Equivalent Circuit &amp; operation</li> </ul>		
Unit 5 Amplifiers and Oscillators	<ul> <li>Classification of Amplifier (Concept of Voltage Amplifier &amp; Power Amplifier)</li> <li>Basic Concept of Feedback in Amplifiers &amp; Oscillator (Concept of Positive &amp; Negative feedback, Barkhausen's criteria)</li> <li>Feedback Amplifier Topologies: Voltage Series, Voltage Shunt Current Series, Current Shunt (Block Diagram only) Properties of negative Feedback, Effect of Negative feedback on Amplifier Characteristics (Points Only)</li> <li>Introduction of Oscillator,</li> <li>Comparison between an Amplifier &amp; an Oscillator,</li> <li>Classification of Oscillators (Sinusoidal oscillators &amp; Non Sinusoidal Oscillators)</li> <li>Basic Principles of RC Oscillators (Phase shift Oscillator), Crystal Oscillator</li> </ul>	10	12

## **ELECTRONICS DEVICES AND CIRCUITS LAB**

## SUGGESTED PRACTICALS/ EXERCISES

S. No.	Practical	Unit No.
1.	Construct the circuit and plot the VI characteristics of the PN Junction Diode, find the cut in voltage	1
2.	Construct the circuit and plot the characteristics of a Zener Diode. Find the breakdown voltage	1
3.	Construct a Half Wave Rectifier and obtain regulation characteristics – Without Filters and with Filters Compare the results	1
4.	Construct a Full Wave Rectifier and obtain regulation characteristics – Without Filters and with Filters Compare the results	1
5.	Construct a Bridge Rectifier and obtain regulation characteristics – Without Filters and with Filters	1
6.	Obtain the Input & Output characteristics of BJT in CB configuration.	2
7.	Obtain the Input & Output characteristics of BJT in CE configuration	2
8.	Obtain the characteristics of DIAC and TRIAC	3
9.	Obtain the VI characteristics of SCR.	3
10.	Simulate half wave, full wave and bridge rectifier using simulation tool like PSpice/ Orcad/ Multisim	1

#### **Reference Books:**

S. No.	Title of Book	Author	Publication
1.	Analog Circuits	A.K. Maini	Khanna Publishing House Ed. 2018 (ISBN: 978-93- 86173-584)
2.	Electronic Devices and Circuits	S. Salivahanan and N. Suresh Kumar	McGraw Hill Education; Fourth edition (1 July 2017) ISBN: 978-9339219505
3.	Electronics Devices and circuit theory	Boyestad & Nashelsky	Pearson Education India; 11 edition (2015) ISBN: 978-9332542600
4.	Electronic Principles	Albert Malvino & David Bates	Tata McGraw Hill Publication 2010 ISBN: 978-007063424
5.	A Textbook of Applied Electronics	R S Sedha	S Chand Publication
6.	Principles of Electronics	V K Mehta	S Chand and company limited

DIPLOMA IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (E03) / DIPLOMA IN ELECTRONICS ENGINEERING (E06)

#### SEMESTER III

COURSE TITLE	:	DIGITAL SYSTEMS
PAPER CODE	:	7457
SUBJECT CODE	:	302
TREORY CREDITS	:	03
PRACTICAL CREDITS	:	02

Course objective: The aim of this course is to help the student to attain the following knowledge through various teaching- learning experiences:

- > Interpret Number system and Boolean algebra.
- > Learn working principle of Logic Gates.
- ➤ Learn working principle of Combinational and Sequential logic circuits
- > Identify Memory Devices

Unit	Sub – Topic	Hours	Marks
Unit 1	Introduction to different number systems- Binary, Octal, Decimal,	14	14
Number	Hexadecimal		
systems &	<ul> <li>Concepts and Significance of each number system</li> </ul>		
boolean	<ul> <li>Conversion from one number system to another</li> </ul>		
algebra	Binary to Decimal, Octal, Hexadecimal and vice versa		
	Decimal to Octal, Hexadecimal and vice versa		
	Octal to Hexadecimal and vice versa		
	<ul> <li>Fractional number conversions in Binary, Decimal, Octal,</li> </ul>		
	Hexadecimal (up to two decimal points)		
	Boolean variables – Rules and laws of Boolean Algebra		
	• Concept of true/false or 1/0, Basic operations: AND, OR, NOT		
	and Truth tables for basic operations		
	Commutative laws, Associative laws, Distributive laws, Identity		
	laws, Complement laws, Idempotent laws, Absorption laws,		
	Duality principle		
	De-Morgan's Theorem		
	First law and Second Law		
	<ul> <li>Applications in simplifying Boolean expressions</li> </ul>		
	<ul> <li>Proof and verification using truth tables</li> </ul>		
	Simplification of expressions:		
	<ul> <li>Algebraic simplification methods using Boolean algebraic rules</li> </ul>		
	and laws		
	• Karnaugh map simplification(2, 3, and 4 variables)		
	Don't care conditions in logic design		
	Advantages and Limitations of K-maps		
Unit 2	Logic Gates – AND, OR, NOT, NAND, NOR, XOR, XNOR	12	14
Logic gates	Symbolic representation, Truth tables, Boolean expressions for		
	each gate		

		1	
	Universal gates (NAND and NOR)		
	Physical implementation of NOT gate using transistors (basic)		
	concept)		
	Implementation of Boolean expressions using gates:		
	Converting Boolean expressions to logic circuits		
	Multi-level gate implementations		
	Using Universal Gates		
	Practical applications of logic gates		
	<ul> <li>Basic digital circuits (majority voter (up to 4 variable), parity</li> </ul>		
	generator (up to 4 variable))		
	Introduction to combinational logic design		
Unit 3	Arithmetic Circuits	12	14
Combinatio	Binary addition and subtraction		
nal logic	• 1's and 2's complement representation		
circuits	Half Adder, Full Adder, Half Subtractor and Full Subtractor		
	<ul> <li>Block Diagram and explanation</li> </ul>		
	<ul> <li>Truth table and Boolean expression</li> </ul>		
	<ul> <li>Cascading full adders (introduction)</li> </ul>		
	Parallel and Series Adders(Block Diagram)		
	Ripple carry adder		
	<ul> <li>Carry look-ahead adder</li> </ul>		
	Encoder and Decoder		
	• Binary encoders (8-to-3 encoder)		
	Priority encoders		
	Binary decoders (3-to-8 decoder)		
	BCD to 7-segment decoder		
	Multiplexer (MUX)		
	• 2-to-1 MUX, 4-to-1 MUX, 8-to-1 MUX		
	Applications of multiplexers		
	o Parallel-to-serial conversion		
	Demultiplexer (DEMUX)		
	• 1-to-2 DEMUX, 1-to-4 DEMUX, 1-to-8 DEMUX		
	Applications of demultiplexers		
	Serial-to-parallel conversion		
Unit 4	Latch	12	14
Sequential	SR latch (circuit using NAND and NOR, working, limitations)	12	1 17
logic	Triggering		
circuits	Edge-triggering and level-triggering		
Circuits	Flip Flops		
	SR, JK with Race Round condition (circuit using NAND and)		
	NOR, working, truth table, limitations, applications)		
	Block diagram, working and application of T, D, Master-Slave		
	JK Flipflop		
	Counters		
	Asynchronous (Ripple) 4-bit Up Counter		
		1	
	Synchronous 4-bit Up Counter     Asynchronous (Pinnle) 4-bit Down Counter		
	Asynchronous (Ripple) 4-bit Down Counter     Symphonous 4-bit Down Counter		
	• Synchronous 4-bit Down Counter		
	Mod-3 Up Synchronous Counters		
	Johnson Counter  P: Count		
	Ring Counter		

	Registers  • 4-bit Shift Register types  • Serial In Serial Out (SISO)  • Serial In Parallel Out (SIPO)  • Parallel In Serial Out (PISO)  • Parallel In Parallel Out (PIPO)		
Unit 5- Memory devices	Classification of Memories  • Volatile vs. Non-volatile memory • Random Access vs. Sequential Access Random Access Memory (RAM) • RAM organization and architecture • Address lines and memory size calculations • Static RAM (SRAM)& Dynamic RAM (DRAM) • Advantages and disadvantages • Refresh cycles in DRAM • Bipolar RAM • DDR RAM (Double Data Rate) • Concept and advantages Read Only Memory (ROM) • Basic Concept of ROM organization • Expanding memory capacity • Programmable ROM (PROM) • Erasable Programmable ROM (EPROM) • Electrically Erasable Programmable ROM (EEPROM) • Flash memory	10	14

## **DIGITAL SYSTEMS LAB**

S.No.	Practical / Exercise	Unit
1.	To verify the truth tables for all logic Gates – NOT, OR, AND, NAND NOR, XNOR	1
2.	Implement and realize Boolean Expressions with Logic Gates	2
3.	Implement Half Adder, Full Adder, Half Subtractor, Full subtractor using ICs	3
4.	Implement parallel and serial full-adder using ICs	3
5.	Design and development of Multiplexer and De-multiplexer using multiplexer ICs	3
6.	Verification of the function of SR,D, JK and T Flip Flops	4
7.	Design SIPO shift registers	4
8.	Construct a Single digit Decade Counter (0-9) with 7 segment display	4
9.	To design a programmable Up-Down Counter with a 7-segment display	4
10.	Study of different memory ICs	5

## **Suggested Learning Sources:**

S.No.	Title of Book	Author	Publication
1	Digital Electronics	Menka Yadav	AICTE
2	Digital Electronics and Systems	Abhishek Bhatt	AICTE
3	Digital principles & Applications	Albert Paul Malvino & Donald P. Leach	McGraw Hill Education; Eighth edition ISBN: 978-9339203405
4	Digital Electronics	Roger L. Tokheim Macmillian	McGraw-Hill Education (ISE Editions); International 2 Revised ed edition ISBN: 978-0071167963
5	Digital Electronics – an introduction to theory and practice	William H. Gothmann	Prentice Hall India Learning Private Limited; 2 edition ISBN: 978-8120303485
6	Fundamentals of Logic Design	Charles H. Roth Jr	Jaico Publishing House; First edition ISBN: 978-8172247744
7	Digital Electronics	R. Anand	Khanna Publications, New Delhi (Edition 2018) ISBN: 978-93-82609445

-10-00

DIPLOMA IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (E03) / DIPLOMA IN ELECTRONICS ENGINEERING (E06)

#### SEMESTER III

COURSE TITLE	:	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION
PAPER CODE	:	7458
SUBJECT CODE	:	303
TREORY CREDITS	:	03
PRACTICAL CREDITS	:	01

**Course objective:** The aim of this course is to help the student to attain the following knowledge through various teaching- learning experiences:

- > Concept of Measurements and Bridges.
- ➤ Learn working principle of DC and AC Potentiometer.
- ➤ Concept of CRO
- > Types and application of various Transducers

Unit	Topics and Sub-topics	Hours	Marks
Unit – I Basics of Measurements and Bridges	<ul> <li>Basics of Measurements: block diagram of measurement system and working of each block, Accuracy, precision, Resolution, sensitivity, linearity, loading effect.</li> <li>Types of Errors.</li> <li>Bridge Measurement: Circuit diagram, working, balanced equation, advantages and disadvantages, application for all bridges</li> <li>DC Bridges – Wheatstone and Kelvin Double Bridge</li> <li>AC Bridges - Maxwell's Bridge, Hay's Bridge, De-Sauty's Bridge</li> </ul>	12	14
Unit-2 Potentiometer	<ul> <li>Diagram, construction, working and application (derivation not required) for:         <ul> <li>Permanent Magnet Moving Coil Instruments (PMMC)</li> <li>Moving Iron type Instruments (MI)</li> </ul> </li> <li>Basics of voltmeter and ammeter</li> <li>Range extension of meter (simple numerical)</li> <li>Concept of energy and power measurement</li> <li>Potentiometer: Basic concept of Potentiometer</li> <li>List types of DC and AC potentiometer, DC Slide Wire Potentiometer (circuit diagram, working and application in calibration of voltmeter and ammeter)</li> </ul>	12	14
Unit-3 Measuring Instruments	Electronic Instruments for Measuring Basic Parameters:     Electronic multi-meter: Block diagram and working ,     Measurement of voltage, current, resistance, using electronic multimeter     Digital voltmeter (potentiometric type),	12	14

	<ul> <li>Q – Meter: Principle of operation, circuit diagram, working and application of Q – Meter</li> </ul>		
Unit-4 Oscilloscopes	<ul> <li>CRO: block diagram of CRO, Cathode ray tube: construction, operation, Vertical deflection system (derivation not required), Horizontal deflection system, need of Delay line, time base generator,</li> <li>Measurement of Amplitude, frequency using CRO, Lissajous patterns for frequency and phase measurement, component testing using CRO,</li> <li>Dual Trace CRO</li> <li>DSO: Block diagram, working and Application</li> </ul>	12	14
Unit-5 Transducers	<ul> <li>Transducers: definition, types of transducers (active &amp; passive, primary &amp; secondary, analog and digital), Selection Criteria, Characteristics, Construction</li> <li>Working Principles and Application of following Transducers:</li> <li>RTD, Thermocouple, Thermistor, LVDT, Strain Gauge Load Cell, Piezoelectric Transducers</li> </ul>	12	14

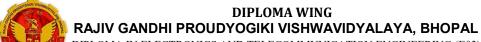
## ELECTRONIC MEASUREMENTS AND INSTRUMENTATION LAB

### **SUGGESTED PRACTICALS/ EXERCISES**

S.No.	Practical / Exercise	Unit
1.	Measure unknown inductance using Maxwell Bridge	1
2.	Measure Low resistance by Kelvin's Double Bridge	1
3.	Calibrate an ammeter using DC slide wire potentiometer	2
4.	Calibrate a voltmeter using DC slide wire potentiometer	2
5.	Calibrate a single-phase energy meter by phantom loading	3
6.	Study the working of Q-meter and measure Q of coils	3
7.	Study working and applications of (i) C.R.O. (ii) D.S.O	4
8.	Measurement of displacement with the help of LVDT	5
9.	Draw the characteristics of the following temperature transducers (a)RTD (Pt-100) (b) Thermistor	5
10.	Measurement of strain/force with the help of strain gauge load cell	5

#### **SUGGESTED LEARNING RESOURCES**

S.No.	Title of Book	Author	Publication
1	Electrical & Electronic Measurement & Instruments	Dr Sudarshan Sahoo	AICTE
2	Electrical & Electronic Measurement & Instruments	A.K. Sawhney	Dhanpat Rai & Sons, India
3	Electronic Instrument and Measurement Technique	W.D. Cooper	Prentice Hall International, India
4	Electronic Instrumentation	J.G. Joshi	Khanna Publishing House
5	Electronic Instrumentation	H. S. Kalsi	Mcgraw-Hill
6	Basic Electrical Measurement	M.B. Stout	Prentice hall of India
7	Electrical and Electronics Measurement and Instrumentation	PrithwirajPukrait, Budhaditya Biswas, Santanu Das, Chiranjib Koley	Mcgraw-Hill
8	Electronic Instrumentation	H. S. Kalsi	Mcgraw-Hill
9	Measurement systems application and design	E.O. Doebelin and D. N. Manik	Megraw-Hill



DIPLOMA IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (E03) / DIPLOMA IN ELECTRONICS ENGINEERING (E06)

#### SEMESTER III

COURSE TITLE	:	ELECTRIC CIRCUITS AND NETWORK
PAPER CODE	:	7459
SUBJECT CODE	:	304
TREORY CREDITS	:	04
PRACTICAL CREDITS	:	00

**Course objective:** The aim of this course is to help the student to attain the following knowledge through various teaching- learning experiences:

- > Concept of electric circuits and network analysis
- > Simplify circuits using Network Theorems
- > Perform Time domain and frequency domain analysis of circuit
- > Interpret basic filters
- > Interpret Two-Port Networks.

Unit	Topics and Sub-topics	Hours	Marks
UNIT 1 Basics of electric circuits and network analysis	<ul> <li>Introduction to Electric Circuits</li> <li>Concepts of current, voltage and power.</li> <li>Concept of resistance, capacitance, inductance</li> <li>Ohm's Law and its applications.</li> <li>Understanding circuit graphs: nodes, branches, and loops as applied to circuit analysis.</li> <li>Analysis of series and parallel resistive circuits (Voltage and current division rules).</li> <li>Mesh Analysis</li> <li>Introduction to mesh (loop) analysis.</li> <li>Applying mesh analysis to solve simple circuit problems.</li> <li>Node Analysis</li> <li>Introduction to node (nodal) analysis.</li> <li>Using node analysis to solve simple circuit problems.</li> <li>Note:(Simple Numericals on DC Resistive Circuits Only)</li> </ul>	12	14
UNIT 2 Network theorems	<ul> <li>Introduction to network theorems and their importance.</li> <li>Analyzing circuits with multiple sources using superposition.</li> <li>Thevenin's Theorem, simplifying complex circuits into a simple equivalent circuit.</li> <li>Norton's Theorem, simplifying complex circuits into a simple equivalent circuit.</li> <li>Maximum power transfer.</li> </ul>	12	14
	Introduction to the Reciprocity Theorem and its application in linear bilateral networks.  Note: DC Resistive Circuit with Independent Sources Only		

UNIT 3	Series and Parallel combination of (i) R (ii) L (iii) C	14	14
Time domain	Time Domain Analysis:		
and frequency	Basic concepts of RC and RL circuits.		
domain analysis	<ul> <li>Charging and discharging of capacitors, growth and decay</li> </ul>		
	of current in inductors.		
	Introduction to transient response in simple circuits. (Unit)		
	Step Input Response Only)		
	Steady state vs. transient state.		
	Initial and final conditions in circuit elements.		
	Frequency Domain Analysis:		
	Introduction to the Laplace Transform and its application in		
	<ul><li>circuit analysis.</li><li>Conversion of circuit equations from the time domain to the</li></ul>		
	frequency domain.		
	Impedance and Reactance in AC Circuits:		
	Calculating impedance for RL, RC, and RLC circuits.		
	<ul> <li>Phasor representation of sinusoidal signals, reactance and</li> </ul>		
	impedance.		
	Introduction to Frequency Domain Concepts:		
	Understanding the concept of frequency domain and its		
	significance. Basics of sinusoidal steady-state analysis.		
	Basics of siliusoidal steady-state alialysis.		
UNIT 4	Introduction to Filters:	10	14
Basics of filter	<ul> <li>Definition and importance of filters in electrical circuits.</li> </ul>		
circuits	<ul> <li>Decibel system, 3-dB bandwidth, pass-band and stop-band</li> </ul>		
	Types of Filters and their applications:		
	<ul> <li>Low-pass, high-pass, band-pass, and band-stop filters.</li> </ul>		
	Simple RC/RL low-pass and high-pass filter design.		
	Understanding the impulse response of basic filter circuits		
	with transfer functions and graphs.		
	Applications of filters in electronics and communication systems.		
UNIT 5	Concept of a two-port network and its applications in circuits:	12	14
Introduction to	Definition and calculation of Z-parameters.		
two-port	<ul> <li>Definition and calculation of Y-parameters.</li> </ul>		
networks	<ul> <li>Definition and importance of transmission parameters.</li> </ul>		
	Note:(Simple Numericals on DC Resistive Circuits Only)		

### SUGGESTED LEARNING RESOURCES

S.No.	Title of Book	Author	Publication
1	Electric Circuits and Networks	Jamuna K. and	AICTE Publishing
		Nilanjan Tewari	
2	A Textbook of Electrical Technology -	B.L. Theraja,	Chand Publishing
	Volume I (Basic Electrical Engineering)		
3	Networks and Systems	Ashfaq Husain	Khanna Publishing House
4	Network Analysis	M. E. Van Valkenburg	Prentice Hall of India
4			
5	Network Analysis & Synthesis	S.P. Ghosh and A K	McGraw Hill Education
3		Chakarborty	
6	Network Analysis And Synthesis	Umesh Sinha,	Satya Prakashan

DIPLOMA IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (E03) / DIPLOMA IN ELECTRONICS ENGINEERING (E06)

#### SEMESTER III

COURSE TITLE	:	PRINCIPLES OF ELECTRONIC COMMUNICATION
PAPER CODE	:	7460
SUBJECT CODE	:	305
TREORY CREDITS	:	03
PRACTICAL CREDITS	:	02

**Course objective:** The aim of this course is to help the student to attain the following knowledge through various teaching- learning experiences:

- > Concept of wireless signal
- > Learn principle of Amplitude and Frequency Modulation
- ➤ Interpret AM and FM receivers
- ➤ Learn principle of PAM and PCM
- > Learn Optical fiber communication

Unit	Topics and Sub-topics	Hours	Marks
Unit 1	<ul> <li>Electromagnetic spectrum</li> <li>Concept of wired and wireless communication</li> <li>Representation of signals in time and frequency domain (double sided of sinusoidal).</li> <li>Introduction to signals, classification of signals- continuous and discrete, periodic and non-periodic, deterministic and random signals. (graphical representation only)</li> <li>Representation of band limited and band pass signal.</li> <li>Noise sources and its classification. noise measurement – SNR, Noise figure.</li> </ul>	10	12
Unit 2	<ul> <li>Introduction to Communication Systems – Modulation – Types – Need for Modulation. Concept of frequency translation.</li> <li>Amplitude Modulation:         <ul> <li>Description of full AM, DSBSC and SSB in time and frequency domains (Mathematical representation of AM wave, modulation index, bandwidth requirement), Power relations in AM. (Simple numerical on modulation index, bandwidth and power).</li> </ul> </li> <li>Frequency Modulation:         <ul> <li>Descriptions of FM signal in time and frequency domains, frequency deviation and modulation index and their relationship. Narrowband and wideband FM. Signal to noise trade off.</li> <li>Phase modulation: Definition and its relation with frequency modulation</li> <li>Comparison of FM and AM</li> </ul> </li> </ul>	14	16

Unit 3	<ul> <li>Generation of AM, Block diagram and description of AM transmitter using low level and high level modulation, Suppression of carrier:         Balanced Modulator (using diode).</li> <li>Detection of AM using Diode detector.</li> <li>AM receiver- Block Diagram of Super heterodyne receiver.</li> <li>IF Amplifier and selection of IF.</li> <li>Generation of FM waves- direct and indirect FM (only Block diagram and basic description)</li> <li>Detection of FM waves: Foster Seeley discriminator</li> </ul>	14	16
Unit 4	<ul> <li>PULSE ANALOG MODULATION-Principle, Generation, detection, bandwidth, advantages, disadvantages and application.</li> <li>Sampling theorem, Types of sampling- Ideal sampling, natural and flat top sampling in time domains.</li> <li>PCM SYSTEMS: Uniform and Non-uniform quantization, Quantization levels, Quantization noise, Encoding. Functional Block Diagram and working of PCM generator.</li> <li>Companding</li> </ul>	10	12
Unit 5	<ul> <li>Optical Communication: Principle of light propagation through fiber, Block diagram of optical fiber communication systems, optical frequency range, advantages, disadvantages and application of optical fiber communication. Intensity Modulation.</li> <li>Multimode and single-mode fibers, step and graded index fiber, acceptance angle, numerical aperture, simple numerical.</li> <li>Types of optical fiber cables.</li> <li>Losses in optical fiber- scattering, UV loss, IR loss, low loss window Concept of Dispersion, effect of Dispersion on Data rate, simple numerical</li> </ul>	12	14

## PRINCIPLES OF ELECTRONIC COMMUNICATION LAB

### SUGGESTED PRACTICALS/ EXERCISES

S.No.	Practical / Exercise	Unit
1	Analyze and classify various signals using an oscilloscope and on simulation software.	1
2	Study the representation of signals in time and frequency domains.	1
3	Measure Signal-to-Noise Ratio (SNR) and Noise Figure of a simple communication	1
	setup.	
4	Generate and detect an AM signal using discrete components	2
5	Analyze power distribution in AM signals.	2
6	Generate and detect an FM signal using direct and indirect FM generation methods	2
7	Study of AM receiver circuit	3
8	Study of FM receiver circuit	
9	Generate and observe a PAM signal.	4
10	Study the PCM encoding and decoding process.	4
11	Study the properties of optical fibers (numerical aperture, acceptance angle).	5
12	Measure attenuation and dispersion in optical fibers.	5

#### **SUGGESTED LEARNING RESOURCES**

S.No.	Title of Book	Author	Publication
1	Electronic Communication Systems	George Kennedy	M.H
2	Fundamentals of communication systems	Proakis & Salehi	Pearson education
3	Principles of Electronic communication	Roddey and Coolen	Pearson education
4	Communication Systems (Analog and Digital)	R.P. Singh, S.D.Sapre	T.M.H.
5	Modern Digital & Analog Communication	B.P. Lathi	Oxford Publications
6	Digital & Analog Communication Systems	K.S. Shanmugam	John Wiley



DIPLOMA IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (E03) / DIPLOMA IN ELECTRONICS ENGINEERING (E06)

#### SEMESTER III

COURSE TITLE	:	SUMMER INTERNSHIP - I
PAPER CODE	:	
SUBJECT CODE	:	
TREORY CREDITS	:	00
PRACTICAL CREDITS	:	02

#### SUMMER INTERNSHIP -

- 3-4 weeks summer internship after 2nd Semester. It should be undertaken in an industry/Govt. or Pvt. Certified Agencies which are in social sector/Govt. Skill Centers/Schemes.
- Evaluation is based on work done, quality of report, performance in viva-voce, presentation etc



DIPLOMA IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (E03) / DIPLOMA IN ELECTRONICS ENGINEERING (E06)

#### SEMESTER III

COURSE TITLE	:	PROFESSIONAL DEVELOPMENT
PAPER CODE	:	
SUBJECT CODE	:	
TREORY CREDITS	:	00
PRACTICAL CREDITS	:	00

#### **Course Objectives:**

Following are the objectives of this course:-

- (1) To learn the principles of Professional and Social ethics.
- (2) To know the concept of Lifelong learning and Self-directed learning.
- (3) To present self for employment.
- (4) To introduce the need of industrial visits.
- (5) To understand CV, Resume, Bio-data and Interview and their significance.
- (6) To develop the skills of Group Discussion.

Unit		Topics and Sub-topics	Hours
Unit-1	<ul> <li>Professional ar</li> </ul>	nd Social Ethics Professional ethics, its need and importance,	4
social	<ul> <li>General code of</li> </ul>	of ethics for engineers, ethical issues for engineers.	
skills	<ul> <li>Need and impo</li> </ul>	ortance of social skills, social skills for better group performance,	
	important soci	ial skills such as social perceptiveness, coordination, negotiation,	
	persuasion etc	2.	
Unit 2	<ul> <li>Lifelong learning</li> </ul>	ng and Self-directed Learning Lifelong learning, its examples,	4
Lifelong	<ul> <li>self-directed le</li> </ul>	earning, its examples, important steps in lifelong learning.	
learning	<ul> <li>Need for plann</li> </ul>	ning self-directed learning, planning self-directed learning plan,	
	examples.		
Unit -3	•	career planning, major career opportunities in concerned branch of	5
Career		tudy of the important career opportunities regarding qualification,	
Planning	knowledge, ski	ills, experience required for them,	
	•	al factors like personal life style, interest areas, desires, personal	
	•	career planning. Identification and detailing of important career	
	• •	in relation to branch of diploma.	
		and detailing of important self-personal factors and self-personal	
		evelopment of self-career plan.	
Unit 4		sposure to environment and practices, lectures by industry experts.	5
Industrial		Students' industrial visits, learning through observing real life	
Visits		ems, planning and organizing the industrial visits.	
Unit 5		Bio-data and Interview Need of presenting self for employment,	6
CV,		es and formats of bio-data, CV, resume, comparison of the three for	
Resume		mitations and specific uses, study of cases and examples of bio-data,	
		d covering letter by all students for self of for the given cases.	
	•	employment related interviews, purpose of interview, dress code,	
	, , ,	and posture of interviewee, do's and don'ts for interviews, interview	
	•	tice of facing employment related interviews for all students.	
Unit 6		ortance of group discussion in professional work, ideal group	6
Group		skills needed to effectively participate in group discussion,	
Discussion	<ul> <li>Practice of gro</li> </ul>	up discussion skills.	